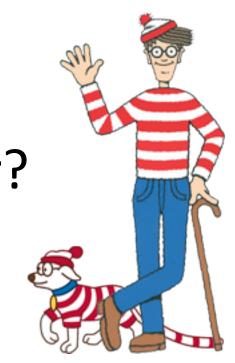
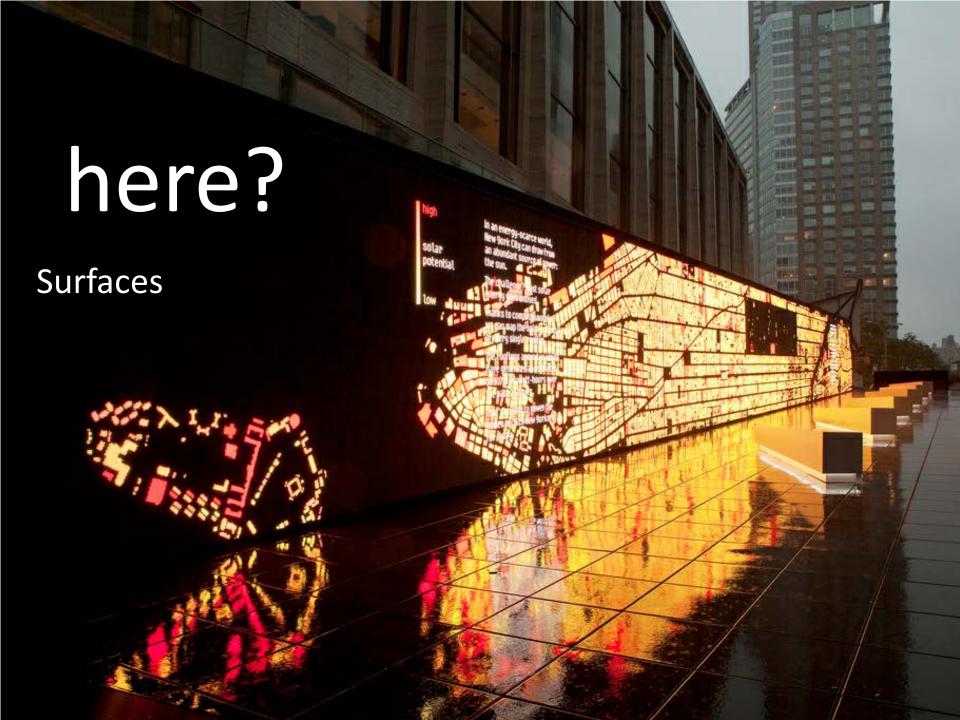
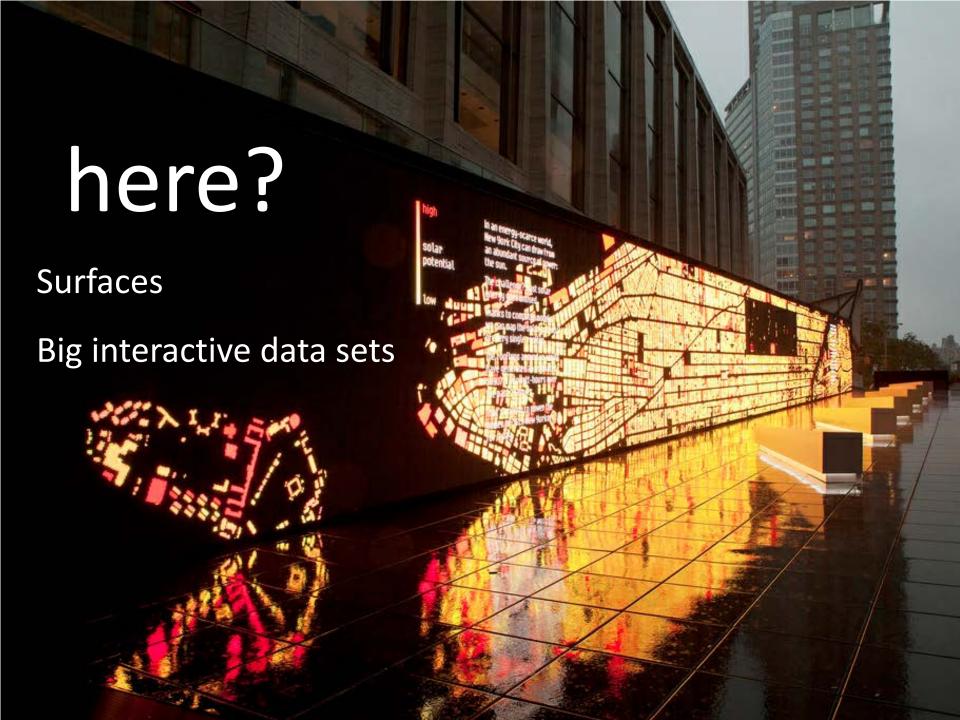
# The Parallel Browser: Synthesis + Parallelization of Layout Engines

Where's the browser?







# can we put it there?

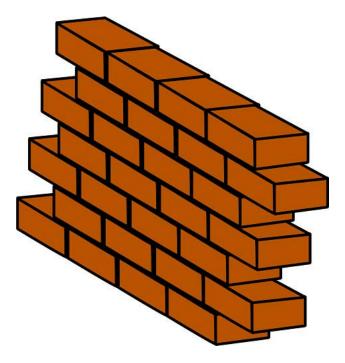




# can we put it there?



#### **Power Wall**



sequential perf = f(power)





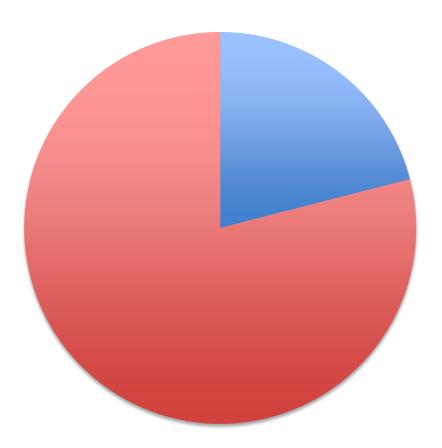


#### Tegra 3, S4, OMAP5, ...

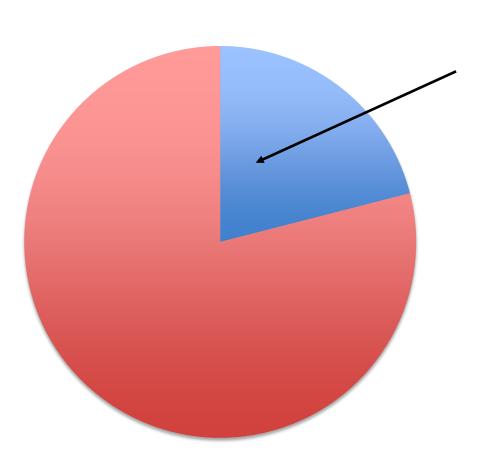


4 core CPU
128b-wide SIMD
12 core GPU

# CPU Profile of the Top 25 Sites

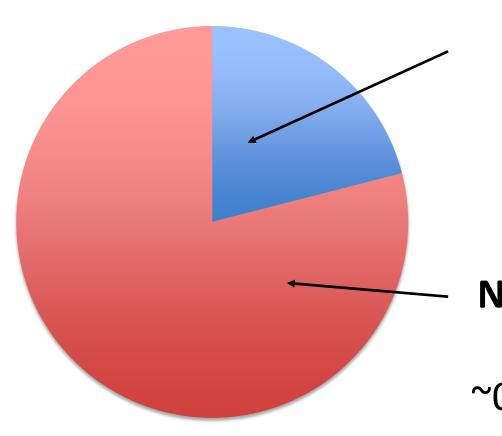


## CPU Profile of the Top 25 Sites



JavaScript: 20% CPU ~30 talks

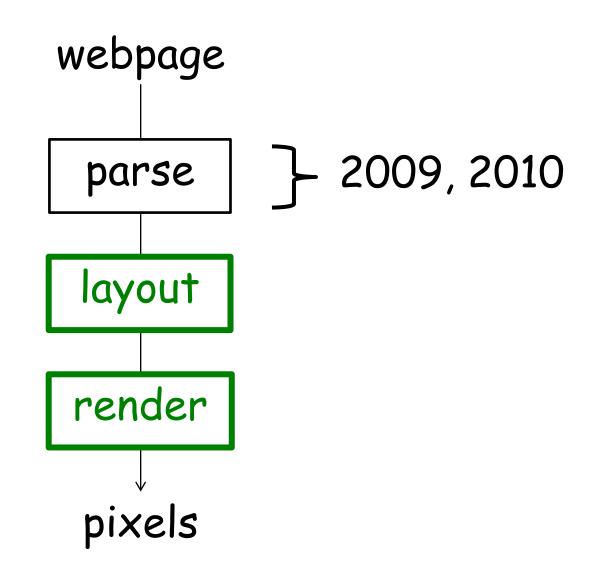
# CPU Profile of the Top 25 Sites

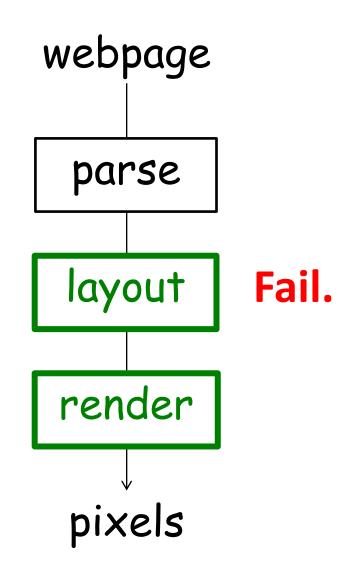


JavaScript: 20% CPU ~30 talks

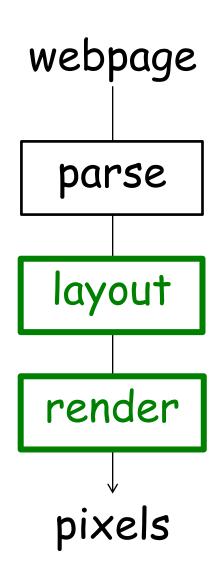
Not JavaScript: 80% CPU
~2 talks
~0 performance solutions



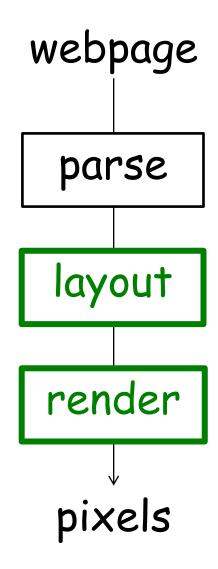


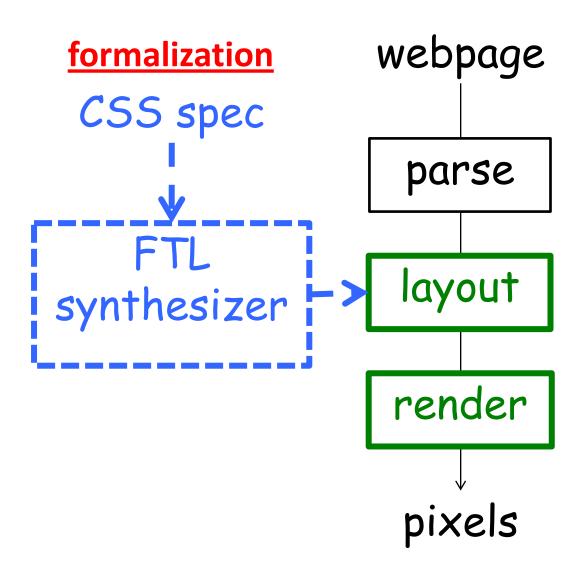


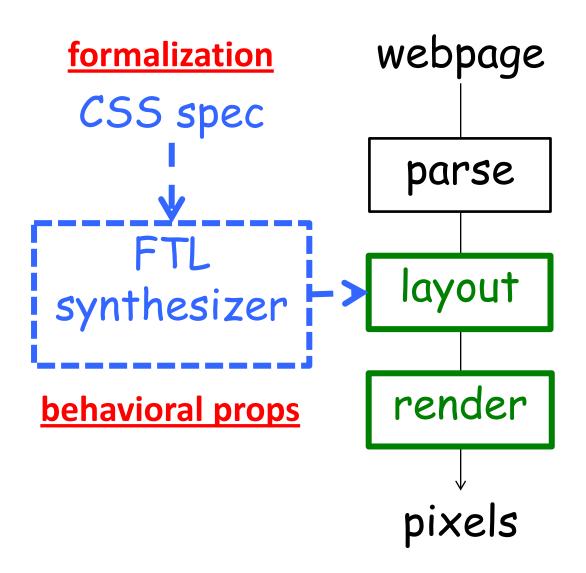
CSS spec

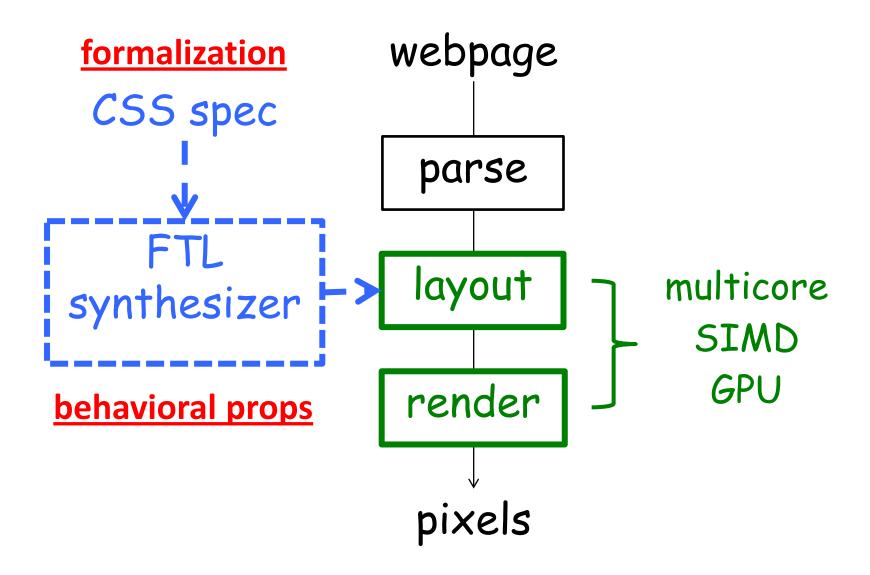


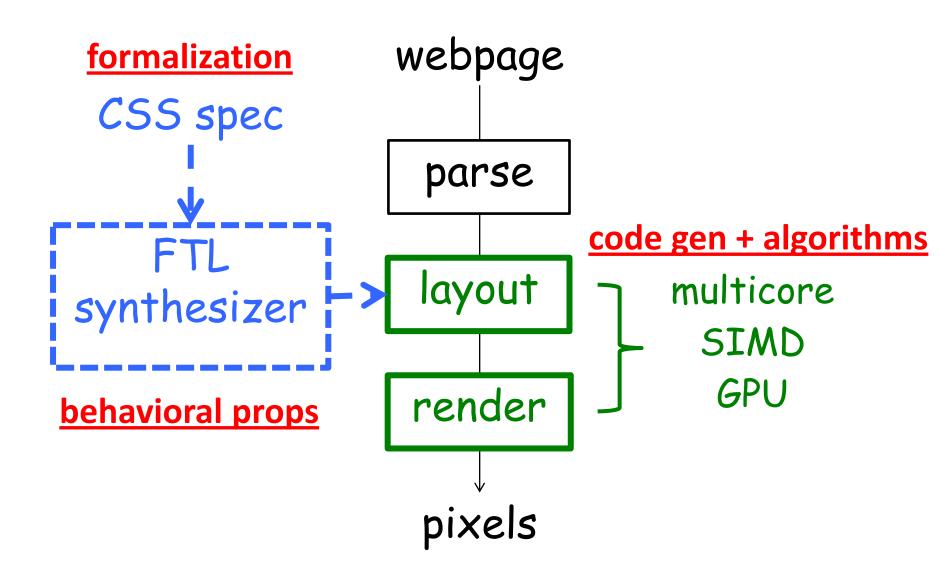
formalization
CSS spec

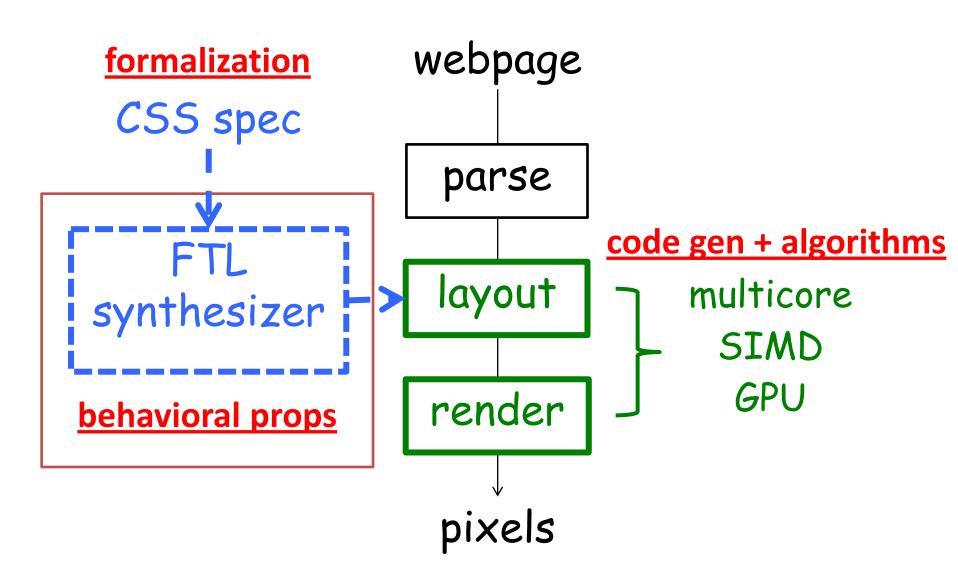


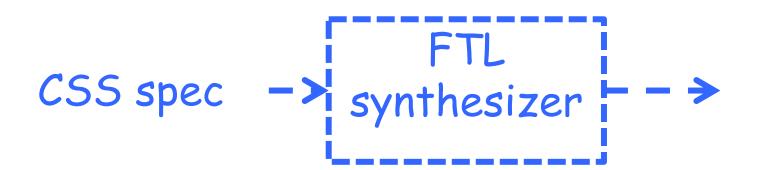


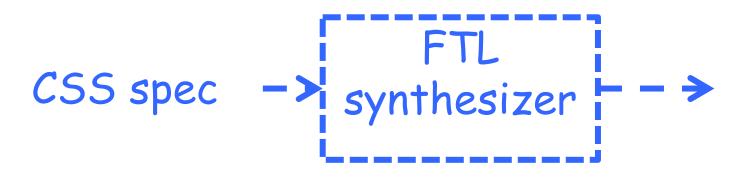




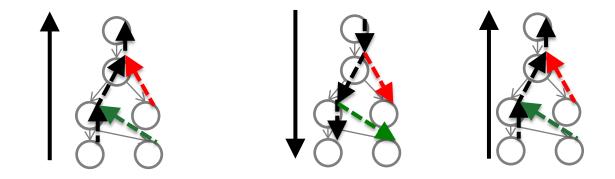








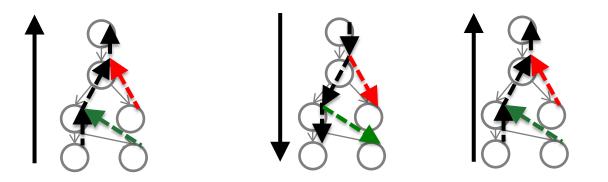
sched: botUp{w,h}; topDown{x,y}; botUp{r}



synthesize sequence of traversal template instantiations

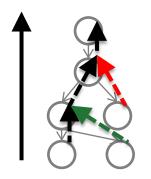
#### Can specify behavior as input instead of as output:

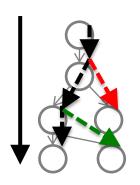
sched: botUp{w,h}; topDown{x,y}; botUp{r}

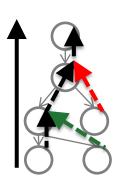


#### Can specify behavior as input instead of as output:

sched: botUp{w,h}; topDown{x,y}; botUp{r}



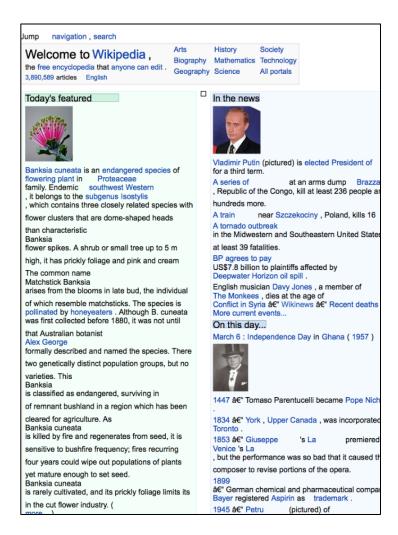




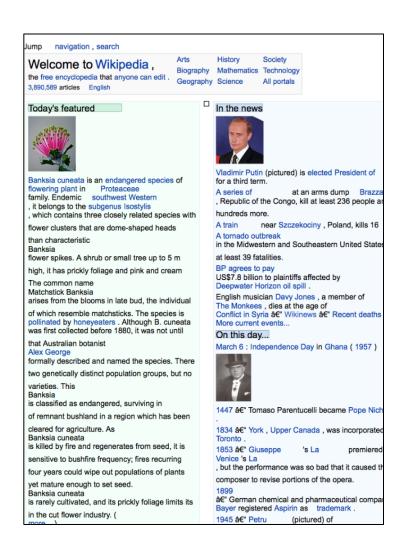
#### Parallelization <u>querying</u>, <u>refinement</u>, and <u>autotuning</u>

sched: (botUp{w} || botUp{h}); ?{?}; inorder{?}

#### **End-to-end Results**



#### **End-to-end Results**



#### Multicore

7x speedup on 8 cores

#### **SIMD**

4x speedup, 4x energy

#### **GPU**

1 million nodes @ 30fps

## Recap

Parallelize 80% case



Spec -> synthesizer -> specialized algorithms

Future: smart UI tools that actually work?